

100

Title: PROCESSOR CORE CLOCK GENERATION CIRCUITS

Inventor: Xiujun Guan

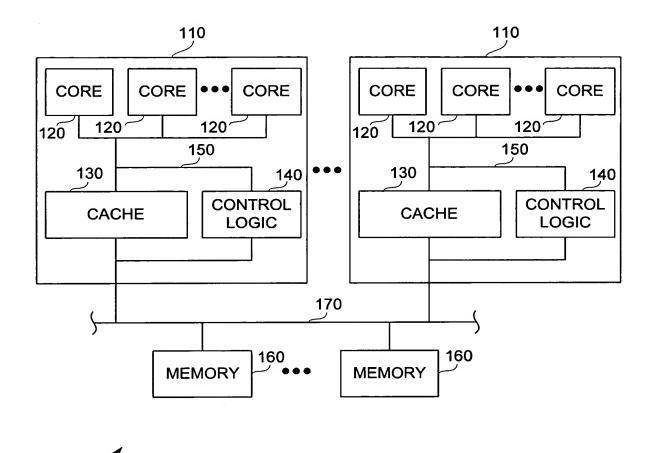


FIG. 1 (Prior Art)

Inventor: Xiujun Guan

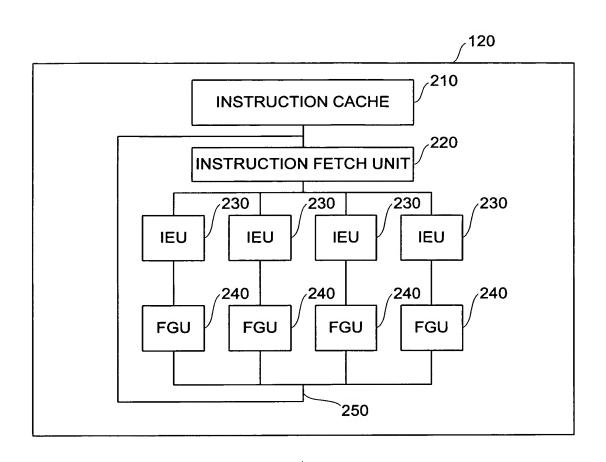


FIG. 2 (Prior Art)

Inventor: Xiujun Guan

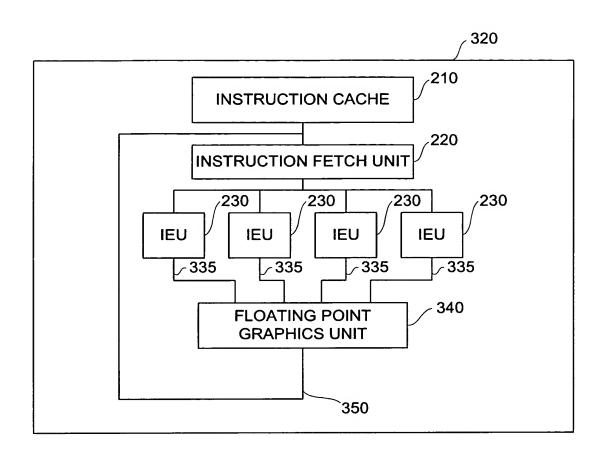


FIG. 3

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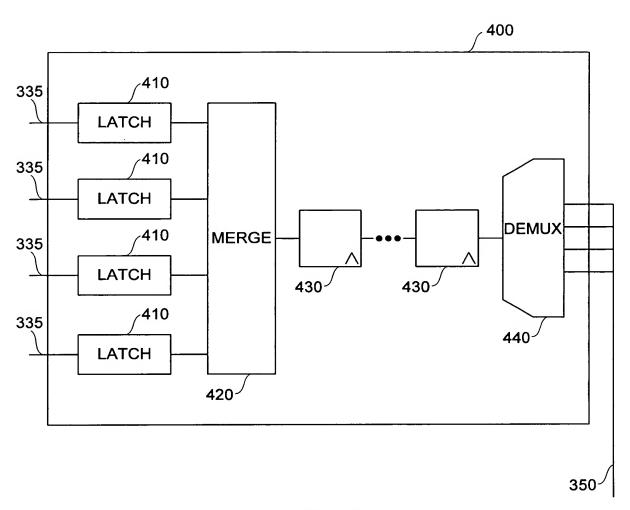
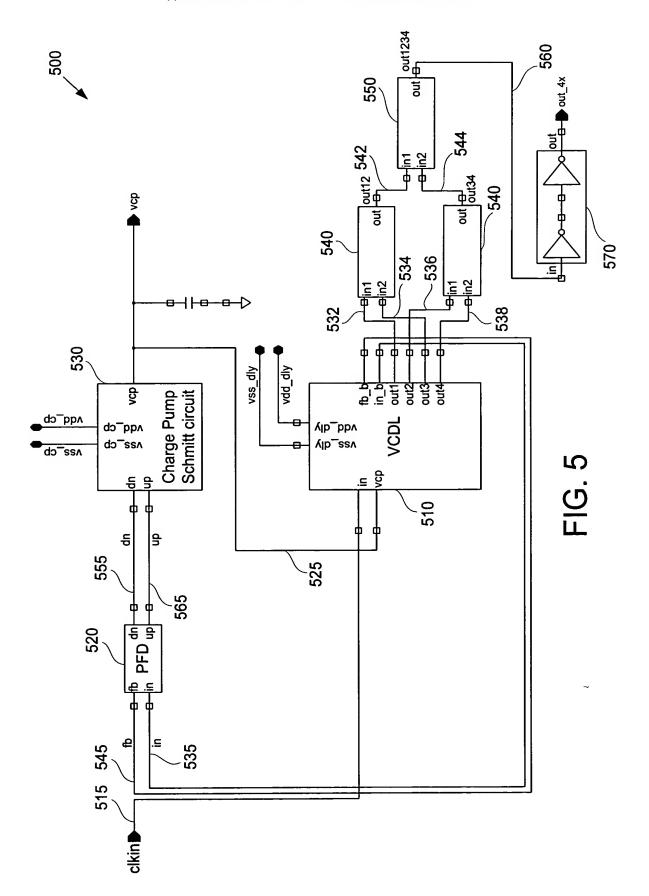


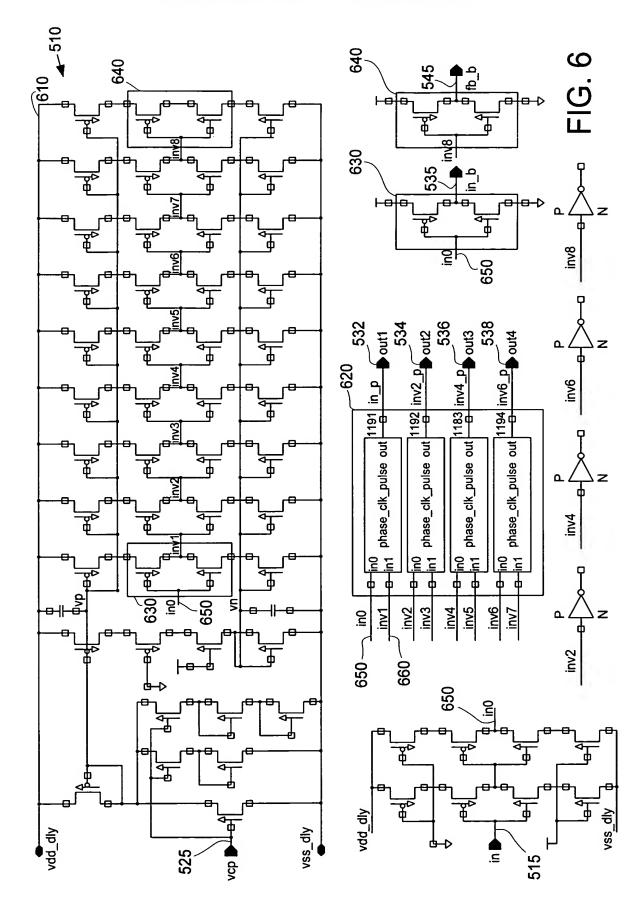
FIG. 4

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Application No: 10/735,104 Docket No: SUNMP348

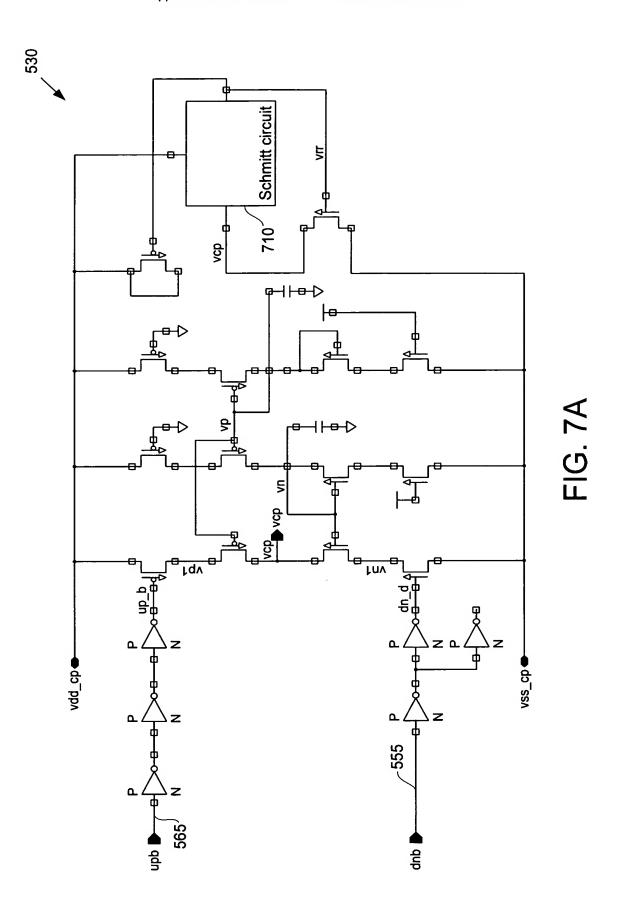
1.



Inventor: Xiujun Guan Application No: 10/735,104 Docket Docket No: SUNMP348



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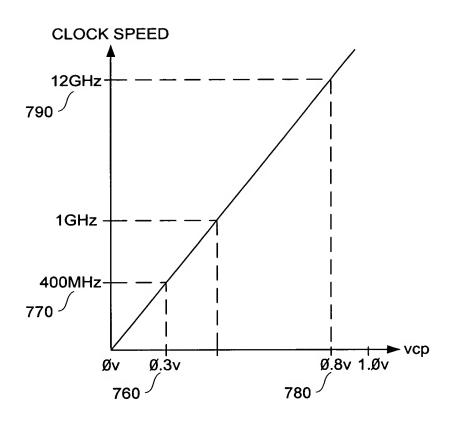


FIG. 7B 700

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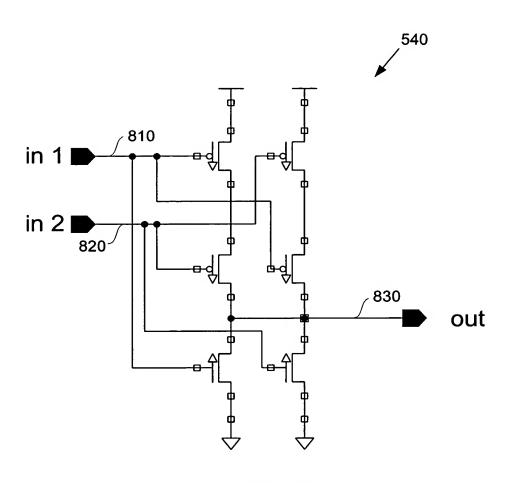


FIG. 8

Inventor: Xiujun Guan

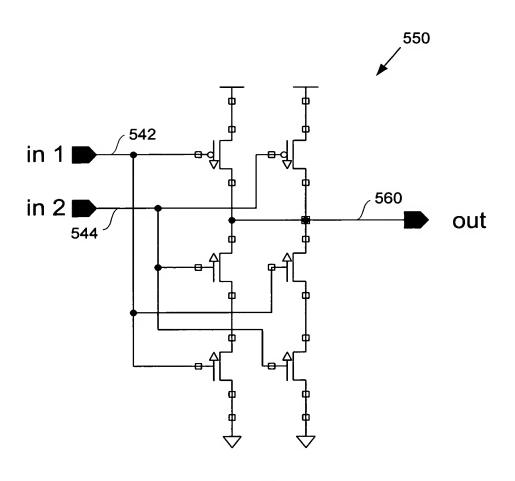


FIG. 9

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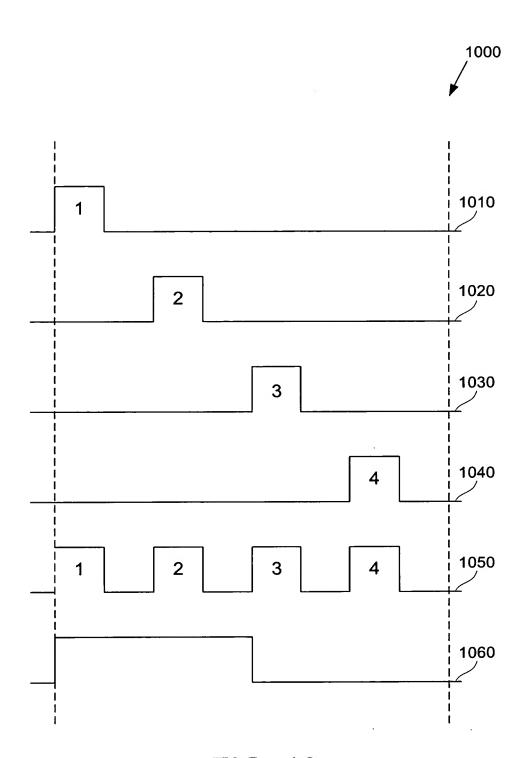


FIG. 10

Inventor: Xiujun Guan

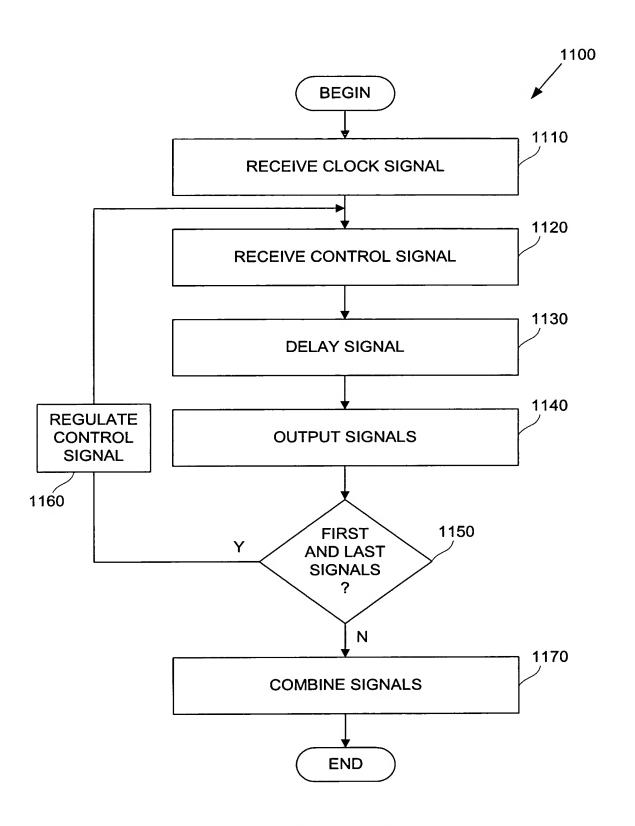


FIG. 11